

a pair of opposed substrates, at least one of said substrates configured to include a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a resin adhesive layer formed on said one of the substrates; and

E1  
cont a driver circuit comprising thin film transistors adhered to said one of the substrates by said resin adhesive layer, wherein said driver circuit is covered by said pair of opposed substrates.

2. (Amended) A liquid crystal display device according to claim 1 wherein each of said thin film transistors has a channel region comprising crystalline silicon.

7. (Amended) An active matrix type liquid crystal display device comprising:

E2 a pair of opposed substrates, at least one of said substrates being provided with a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a resin adhesive layer formed on said one of the substrates;

a driver circuit comprising thin film transistors adhered to said one of the substrates by said resin adhesive layer, wherein said driver circuit is covered by said pair of opposed substrates; and

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a passivation film covering said driver circuit and having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit.

8. (Amended) A liquid crystal display device according to claim 7 wherein each of said thin film transistors has a channel region comprising crystalline silicon.

15. (Amended) An active matrix type liquid crystal display device comprising:

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a pair of opposed substrates, at least one of said substrates being provided with a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a driver circuit comprising thin film transistors adhered to said one of the substrates by a resin layer, wherein said driver circuit is covered by said pair of opposed substrates; and

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cont.  
a passivation film covering said driver circuit, said passivation film having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit, wherein said contact hole has a tapered configuration.

16. (Amended) A liquid crystal display device according to claim 15 wherein said passivation film comprises a first silicon oxide layer formed over said thin film transistors, and a second silicon oxide layer formed on said first silicon oxide layer, said second silicon oxide layer having a larger etching rate than said first silicon oxide layer.

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17. (Amended) A liquid crystal display device according to claim 15 wherein each of said thin film transistors has a channel region comprising crystalline silicon.

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22. (Amended) An active matrix type liquid crystal display device comprising:

a pair of opposed substrates, at least one of said substrates being provided with a pixel circuit for switching pixels of said display device;

a liquid crystal material disposed between said pair of opposed substrates;

a driver circuit comprising thin film transistors adhered to said one of the substrates by a resin, wherein said driver circuit is electrically coupled to said pixel circuit through a metal bump, and wherein said driver circuit is covered by said pair of opposed substrates.

36. (Amended) An active matrix type display device comprising:

a substrate;

a pixel circuit formed over said substrate for switching pixels of said display device,

a driver circuit comprising thin film transistors formed over said substrate and from a stick substrate separate from said substrate, wherein said thin film transistors are coupled in said driver circuit after being peeled from said stick substrate;

a resin adhesive layer for adhering said thin film transistors to said substrate; and

a sealing member over said substrate, wherein said sealing member encloses said pixel circuit and said driver circuit.

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37. (Amended) A display device according to claim 36 wherein each of said thin film transistors has a channel region comprising crystalline silicon.

44. (Amended) An active matrix type display device comprising:

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a substrate;

a pixel circuit formed over said substrate for switching pixels of said display device

E7

a driver circuit comprising thin film transistors adhered to said substrate by a resin;

a passivation film covering said driver circuit, said passivation film having a contact hole to allow an electrical connection between at least one of said thin film transistors and said pixel circuit; and

a sealing member over said substrate, wherein said sealing member encloses said pixel circuit and said driver circuit.

46. (Amended) A display device according to claim 44

E 8 wherein each of said thin film transistors has a channel region comprising crystalline silicon.

55. (Amended) An active matrix type display device comprising:

a substrate;

a pixel circuit for switching pixels of said display device;

E 9 a driver circuit comprising thin film transistors formed over said substrate, a resin adhering said thin film transistors to said substrate, and a metal bump electrically coupling said driver circuit to said pixel circuit; and

a sealing member over said substrate, wherein said sealing member encloses said pixel circuit and said driver circuit.

56. (Amended) A display device according to claim 55

E 10 wherein each of said thin film transistors has a channel region comprising crystalline silicon.--

Please add claims 65-71.

-- 65. (New) A liquid crystal display device according to claim 1 wherein said driver circuit is formed from a stick substrate separate from said pair of substrates.

66. (New) A liquid crystal display device according to claim 7 wherein said driver circuit is formed from a stick substrate separate from said pair of substrates.

67. (New) A liquid crystal display device according to claim 15 wherein said driver circuit is formed from a stick substrate separate from said pair of substrates.

68. (New) A liquid crystal display device according to claim 22 wherein said driver circuit is formed from a stick substrate separate from said pair of substrates..

69. (New) A display device according to claim 36 wherein said driver circuit is formed from a stick substrate separate from said substrate.

70. (New) A display device according to claim 44 wherein said driver circuit is formed from a stick substrate separate from said substrate.

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71. (New) A display device according to claim 55 wherein  
said driver circuit is formed from a stick substrate separate  
from said substrate.--

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